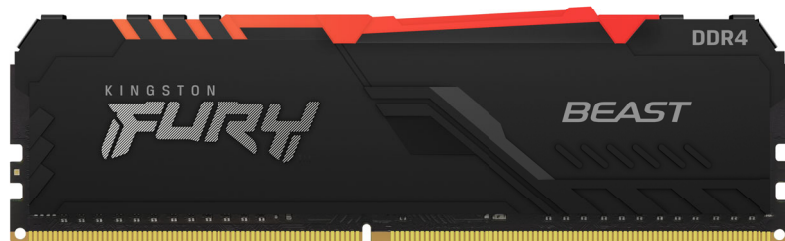


Memory Module Specifications

KF426C16BB1A/16

16GB 2G x 64-Bit

DDR4-2666 CL16 288-Pin DIMM



SPECIFICATIONS

CL(IDD)	16 cycles
Row Cycle Time (tRCmin)	45.75ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	260ns(min.)
Row Active Time (tRASmin)	29.25ns(min.)
UL Rating	94 V - 0
Operating Temperature	0° C to +70° C
Storage Temperature	-40° C to +85° C

DESCRIPTION

FURY KF426C16BB1A/16 is a 2G x 64-bit (16GB) DDR4-2666 CL16 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 1G x 8-bit FBGA components per module. Each module kit supports Intel® Extreme Memory Profiles (Intel® XMP) 2.0. Each module has been tested to run at DDR4-2666 at a low latency timing of 16-18-18 at 1.2V. Additional timing parameters are shown in the Plug-N-Play (PnP) Timing Parameters section below. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

Note: The PnP feature offers a range of speed and timing options to support the widest variety of processors and chipsets. Your maximum speed will be determined by your BIOS.

FEATURES

- Power Supply: VDD = 1.2V Typical
- VDDQ = 1.2V Typical
- VPP = 2.5V Typical
- VDDSPD = 2.2V to 3.6V
- On-Die termination (ODT)
- 16 internal banks; 4 groups of 4 banks each
- Bi-Directional Differential Data Strobe
- 8 bit pre-fetch
- Burst Length (BL) switch on-the-fly BL8 or BC4(Burst Chop)
- Height 1.62" (41.2mm), w/heatsink

FACTORY TIMING PARAMETERS

- Default (Plug N Play): DDR4-2666 CL16-18-18 @ 1.2V
- XMP Profile #1: DDR4-2666 CL16-18-18 @ 1.2V

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The diagram shows a two-story building layout. The top floor (attic) contains four rooms on the left and four rooms on the right, with a central corridor. The bottom floor (main floor) also contains four rooms on the left and four rooms on the right, with a central corridor. The layout is symmetrical, with rooms and corridors arranged in a grid-like fashion. The rooms are represented by dark gray rectangles, and the corridors are represented by lighter gray lines. The building has a central entrance on the bottom floor, marked by a small white rectangle.

Technical drawing of a bridge deck cross-section. The drawing shows a horizontal section with various dimensions and details. The total width is 133.35. The width of the deck slab is 129.55. The width of the deck slab at the bottom is 2.10 ± 0.15. The width of the deck slab at the top is 3.00. The width of the deck slab at the bottom is 2.70 ± 0.15. The width of the deck slab at the bottom is 3.35. The width of the deck slab at the bottom is 28.90. The width of the deck slab at the bottom is 64.60. The width of the deck slab at the bottom is 56.10. The width of the deck slab at the bottom is 22.95. The width of the deck slab at the bottom is 13.60. The width of the deck slab at the bottom is 7.50. The width of the deck slab at the bottom is 8.00. The width of the deck slab at the bottom is 11.00. The drawing includes details A, B, C, D, and E. Detail A is a circular detail. Detail B is a circular detail. Detail C is a circular detail. Detail D is a circular detail. Detail E is a circular detail. The drawing also shows pins 1, 35, 47, 105, and 117. The drawing is a technical drawing of a bridge deck cross-section.

All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published FURY memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.